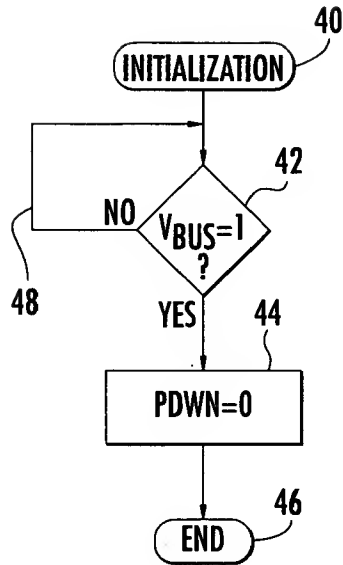
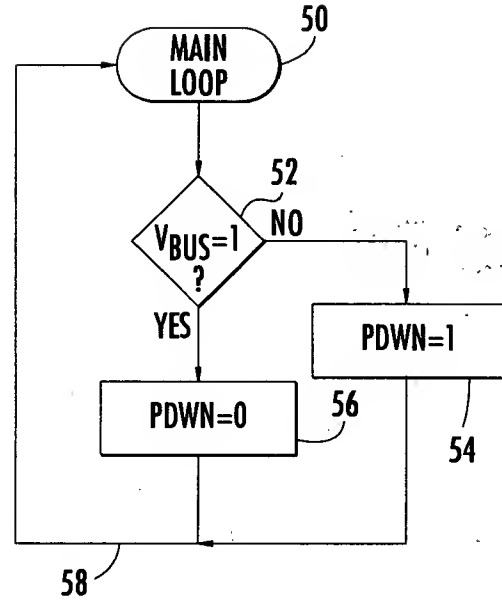




**FIG. 1.**  
**PRIOR ART**



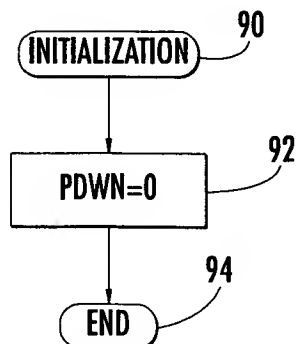
**FIG. 2.**  
**PRIOR ART**



**FIG. 3.**  
**PRIOR ART**

VBUSSTAT	PDWN	STATE OF REGULATOR 26
0	0	STOP
0	1	STOP
1	0	GO
1	1	STOP

**FIG. 4.**



**FIG. 5.**

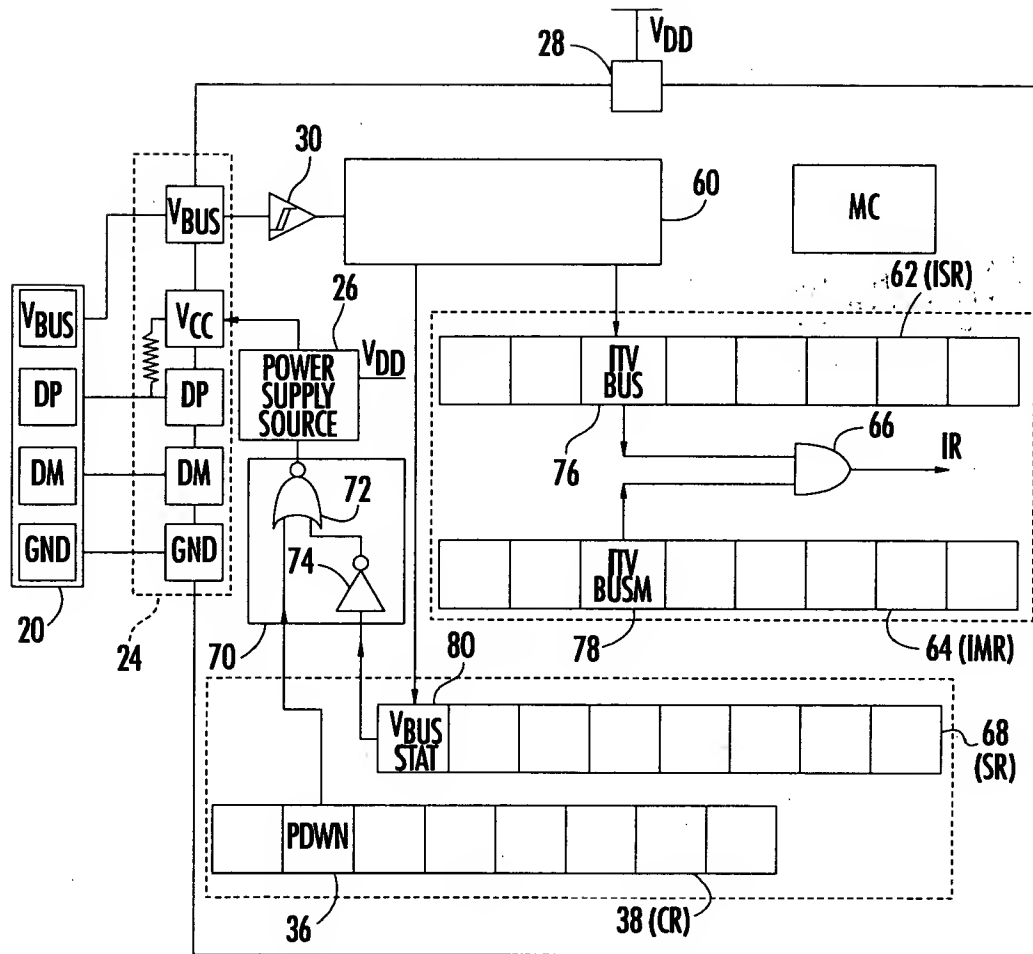


FIG. 6.

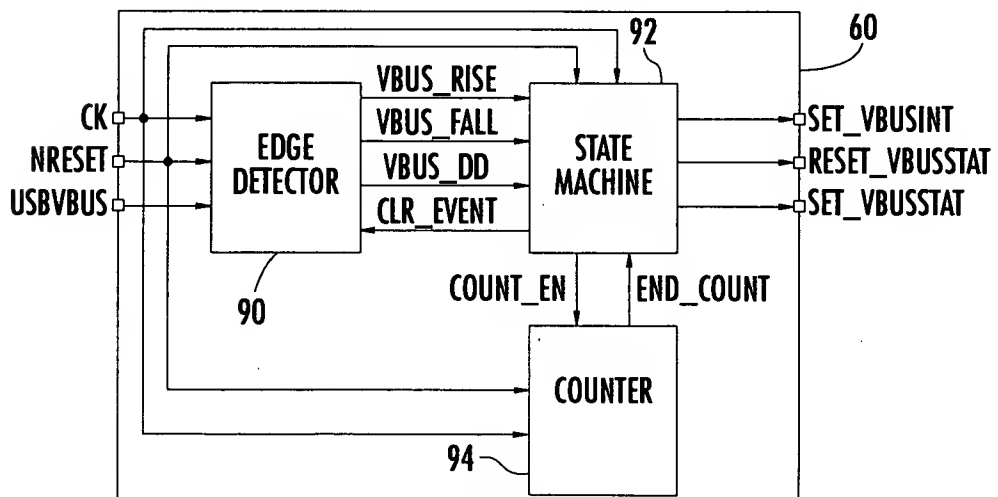


FIG. 7.

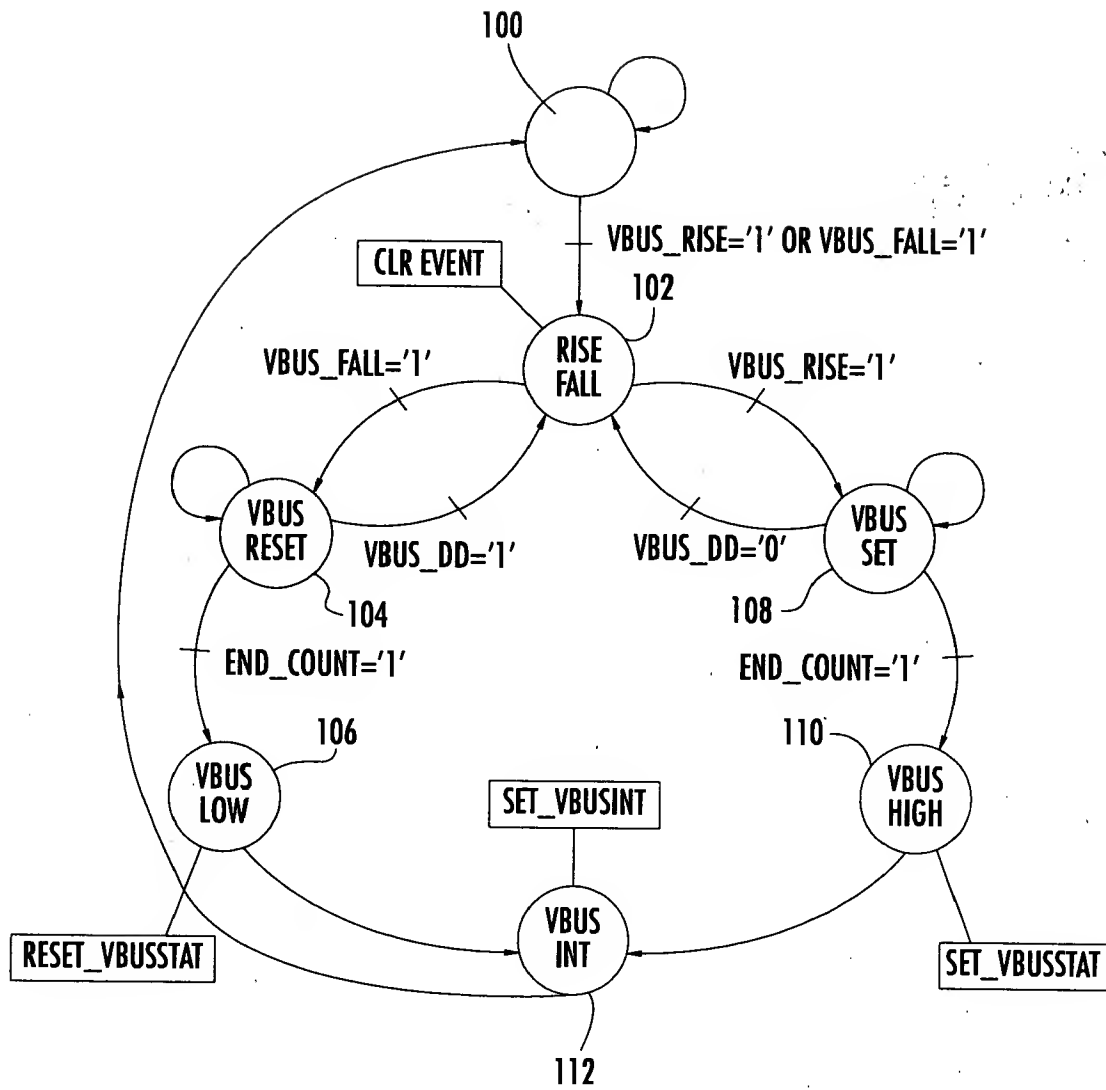


FIG. 8.